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Utility Patent  
Ser. No. 09/921,375

**CLAIM AMENDMENTS**

Please amend the claims (~~striketrough~~ indicating deletion and underline indicating insertion) as follows:

1-11. (Cancelled).

12. (Currently Amended) A method to utilize a digital, wireless PC/PCS modem having an antenna attached to a PCMCIA card-type interface in communication with an integrated circuit board, said modem works in conjunction with a computer provided with a swivel-based camera, a microphone and at least three tuner cards to relay wireless communications via satellite, said method comprises the steps:

passing the digital signals transmitted via a satellite link and a wireless relay system from said antenna that receives said signals to a series of line amplifiers, said series of line amplifiers and a network switching element have an input buffer coupled therebetween, said network switching element receives input from said PC/PCS modem, said network switching element has a frequency/feedback along with a channel/screen selection function flowing from said switching network bi-directionally to

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a multi-tuner;

passing the data received from said multi-tuner module to a microprocessor;

and

passing said data on to a universal asynchronous receiver transmitter via a first bi-directional path, said universal asynchronous receiver transmitter is responsible for all data transfers from a computer system to the computer system's modem output system,

wherein said data transfer occurs between all modules through a series of parallel bus, a series of serial transmit bus and a series of serial receive bus.

13. (Currently Amended) The method in Claim 12 further comprises the steps:

aligning said data in a proper configuration by means of a micro controller;

processing said proper configuration by means of a voice, a data, a fax and a video processor through a ~~through a~~ second parallel bus, a second serial transmit bus and a second serial receive bus, said voice, data, fax and video processor includes a digital signal processing support module used as a prebuffer into a digital signal processor, and wherein said digital signal processor performs all necessary operations on said data, including handshake verification, through a series of built-in algorithms.

14. (Cancelled).